

964 DFDR III BOARD

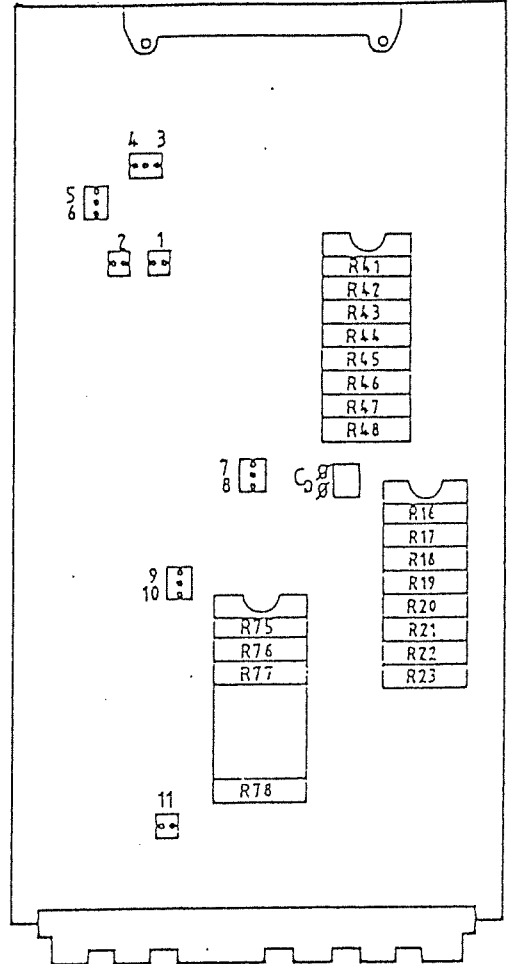
TYPE 964- POSITION 2

JUMPERS

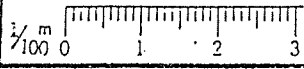
NO.	INSTALLED	FUNCTION
1		DIFFERENTIAL DRIVE
2		DIFFERENTIAL DRIVE
3	YES	STEER DRIVE (TPOL)
4		DIFFERENTIAL DRIVE (TPOL)
5	YES	ROT SPEED TO D REF
6		ROT SPEED FIXED (+5 VOLT)
7	YES	LSE FROM OFFSET
8		LSE FROM S+
9	YES	STEER DRIVE
10		DIFF. DRIVE (RAMP 3 AT MAN STEER)
11		QUAD OFFSET

RESISTORS

NO.	VALUE	FUNCTION		SOFTWARE SELECT	
				P00	P01
	単位: kΩ				
R16		DERIVATION	4	1	1
R17	820	GAIN	1	0	0
R18		DERIVATION	3	0	1
R19		GAIN	2	1	0
R20		DERIVATION	2	1	0
R21		GAIN	3	0	1
R22	OPEN	DERIVATION	1	0	0
R23		GAIN	4	1	1
R41	0~10 (POT)	SPEED (MAX)			
R42	820	STEEPER SLOPE/RAMP (CPU)			
R43	1,000	STOP/START (SLOPE OR RAMP)			
R44		ROTATION SPEED			
R45		FEEDBACK POT (GAIN AT ROTATION)			
R46		INTEGRATION (TIME)			
R47	1,000	MAN POT (GAIN)			
R48		FEEDBACK POT (GAIN AT MAN)			
R75	10	INTEGRATION (AMPLITUDE)			
R76	20	LARGE STEERING ERROR			
R77	15+0~5 (POT)	RIGHT TACH FEEDBACK SCALING			
R78	18	LEFT TACH FEEDBACK SCALING			
C0		DIFFERENTIATOR CUT-OFF FREQUENCY			



川鉄下ドック 氏名 MCH. NAME			
殿 DFDRボード設定表			
技安 APPD	担当 DRAWN	尺原 SCALE	日付 DATE
中尾		Trce	20-May-'94
主任 CHKD	担当 DRAWN	換装 MCH. CD	箱番 BOX. NO
西田	坂本		
		図面番号 DRG. NO	第3角法 3RD ANGLE
		E.4.9.4.6.9.5.4.	部組 PA. CD
		試用図面 (保管) 番号 OLD-DRG. (KEEP) NO	
		◎ 三井三池製作所 MITSUI MIKE	



数量 QTY	製番 MANUF. NO
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1014 QUAD III BOARD

TYPE 1014-

POSITION 3

JUMPERS

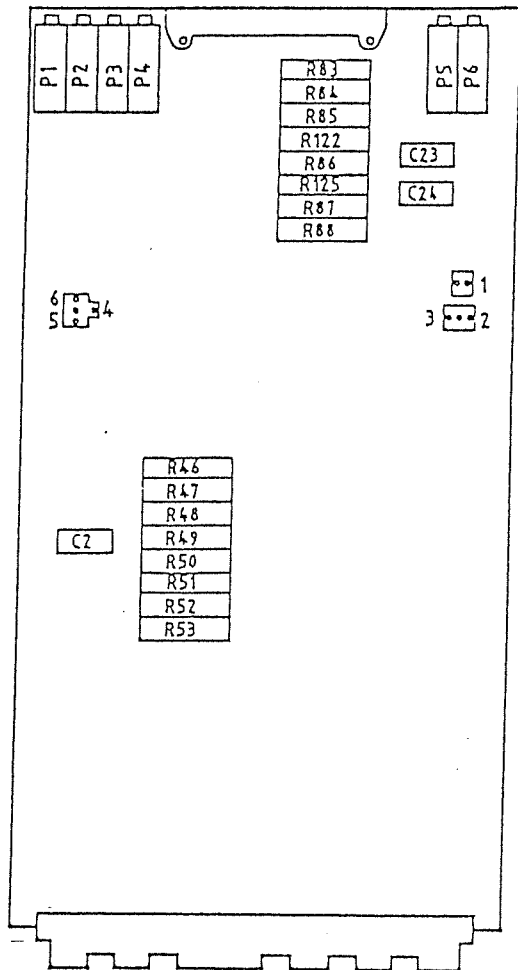
NO.	INSTALLED	FUNCTION
1		"B" DRIVE TURNS OPPOSITE "A" DRIVE
2		"B" DRIVE TURNS OPPOSITE "A" DRIVE
3	YES	"B" DRIVE TURNS SAME AS "A" DRIVE
4		FEEDBACK POTS ON/OFF WITH DIFF DRIVE SIGNAL
5	YES	FEEDBACK POTS -ON/OFF WITH FIXED ANT/MAN SIGNAL
4+5		SPECIAL JUMPER USING DIODES (FOR MOVING ANT. IN SD MODE)
6		FEEDBACK POT ALWAYS ON

RESISTORS

NO.	VALUE	FUNCTION	SOFTWARE SELECT	
			ME4	SE4
P1	YES	ADJUSTS +90° REF S+B	1	1
P2		ADJUSTS -90° REF S+A	1	0
P3	YES	ADJUSTS +90° REF S-A	1	1
P4		ADJUSTS -90° REF S+B	1	0
P5	YES	ADJUSTS 0° TRIM S+A		
P6	YES	ADJUSTS 0° TRIM S+B		

単位: KΩ

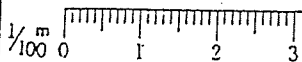
NO.	VALUE	FUNCTION	SOFTWARE SELECT	
			PDO	PD1B
R46	820	GAIN 0	0	0
R47		GAIN 1	1	0
R48		GAIN 2	0	1
R49		GAIN 3	1	1
R50		DERIVATION 3	1	1
R51		DERIVATION 2	0	1
R52		DERIVATION 1	1	0
R53	OPEN	DERIVATION 0	0	0
R83	820	GAIN S+A		
R88	820	GAIN S+B		
R84		F.B. PROPORTIONAL GAIN SEL POT A		
R87		F.B. PROPORTIONAL GAIN SEL POT B		
R85	100	F.B. PROPORTIONAL GAIN POT A		
R86	100	F.B. PROPORTIONAL GAIN POT B		
R122		F.B. DERIVATING GAIN POT A		
R125		F.B. DERIVATING GAIN POT B		



CAPACITORS

NO.	VALUE	FUNCTION
C2		DIFFERENTIATOR CUT-OFF FREQUENCY
C23		F.B. DIFFERENTIATOR CUT-OFF FREQ POT A
C24		F.B. DIFFERENTIATOR CUT-OFF FREQ POT B

川鉄工業株式会社		機名 MCH. NAME	
殿		QUADボード設定表	
課長 APPD 中尾	担当 DRAWN	尺法 SCALE free	日付 DATE 20-11-99
主任 CHKD 西田	担当 DRAWN 坂本	機種 MCH. CD	箱番 BOX. NO
		図面番号 DRG. NO E.4.9.4.6.9.5.4.	第3角法 3RD ANGLE 部組 PA. CD
		④ 三井三池製作所 流用図面 (保管) 番号 OLD-DRG. (KEEPING)	
MITSUI. MIIE			



数量 QTY 製法 MANUF. NO

整理番号

A 4 (297×210)

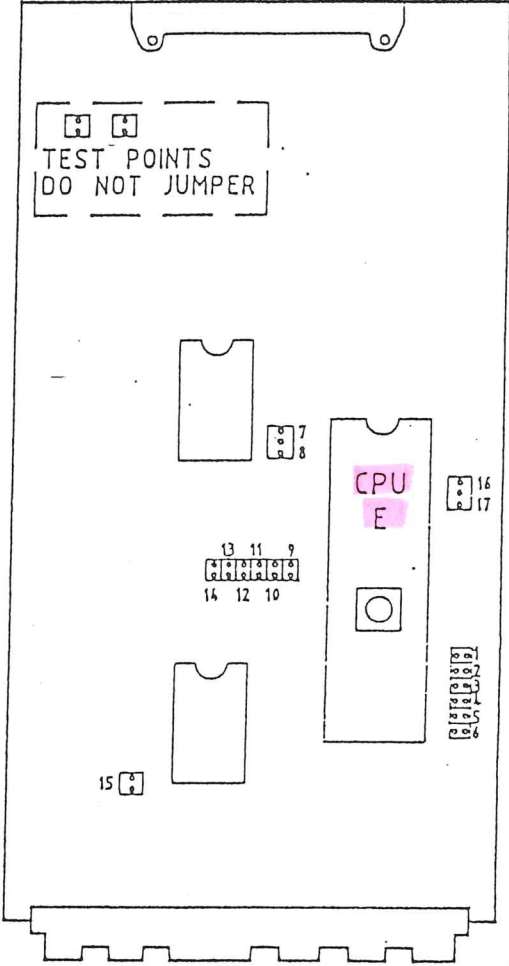
898 CPU BOARD

TYPE 898-

POSITION 4

JUMPERS

NO.	INSTALLED	FUNCTION
1	YES	SELECTED CARRIER IDENTITY
2		SELECTED CARRIER IDENTITY
3		SELECTED CARRIER IDENTITY
4		SELECTED CARRIER IDENTITY
5		SELECTED CARRIER IDENTITY
6		SELECTED CARRIER IDENTITY
7		CRYSTAL 1 TO BAUD GENERATOR
8	YES	CRYSTAL 2 TO BAUD GENERATOR
9		2400 BAUD MAIN
10		1200 BAUD MAIN
11	YES	600 BAUD MAIN
12	YES	600 BAUD SLAVE
13		1200 BAUD SLAVE
14		2400 BAUD SLAVE
15		WATCHDOG DISABLE
16	YES	NORMAL COMMUNICATION
17		EXTERNAL COMMUNICATION
18		
19		
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31		

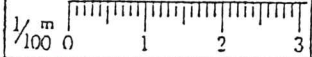


メモリ揮発
4A41C
IEVE 9142

RESISTORS

NO.	VALUE	FUNCTION
1		
2		
3		
4		
5		

POS	PROM
E	CPU MC 68701 V4S



川鉄下板 7 機名 MCH. NAME			
殿 CPUボード設定表			
課長 APPD 中尾	担当 DRAWN	尺度 SCALE free	日付 DATE 20-May-'94
主任 CHKD 西田	担当 DRAWN 坂本	機種 MCH. CD	第 3 角法 3RD ANGLE
数量 QTY	製造 MANUF. NO	箱番 BOX. NO	部組 PA. CD
MITSUI MIKE		図面番号 DRG. NO E.4:9.4:6.9.5.4	流用図面 (保管) 番号 OLD-DRG. KEEPING 13/5

897 I/O II BOARD

TYPE 897-

POSITION 7

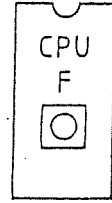
JUMPERS

NO.	INSTALLED	FUNCTION	FUNCTION
1	YES	INPUT 7	TRVB:7 / 自動エ-ト後進
2			ONで自動後進可
3	YES	INPUT 11	FRQ0:B / 周波数選択 0
4			ONで F1, F3
5	YES	INPUT 14	
6			
7	YES	INPUT 10	
8			
9	YES	INPUT 8	ESTP:8 / 非常停止
10			OFFで急制動, 停止
11	YES	INPUT 2	
12			
13	YES	INPUT 5	CSUS:5 / 停止
14			OFFで 急制動 = 停止
15	YES	INPUT 4	CSUR:4 / 減速
16			OFFで 低速(3分)に制限
17	YES	INPUT 1	MNBW:7 / 自動後進
18			ONで 自動時 後進選択
19	YES	INPUT 3	
20			
21	YES	INPUT 13	
22			
23	YES	INPUT 6	TRVF:6 / 自動エ-ト前進
24			ONで 自動前進可
25	YES	INPUT 9	
26			
27	YES	INPUT 12	
28			
29	YES	INPUT 15	FRQ1:F / 周波数選択 1
30			ONで F2, F3
31		DISABLE WATCHDOG	

CORRESPONDING
MATE-N-LOK PIN
NUMBER FOR K7
CONNECTOR WITHIN
PARENTHESIS

31

- (1) 17 15 23
- (4) 15 12 27
- (5) 13 14 19 25
- (2) 11 12 23
- (8) 9 10 13 21
- (10) 7 8 3 19
- (14) 5 6
- (11) 3 6
- (7) 1 2



ODD NO:
JUMPER TO GND

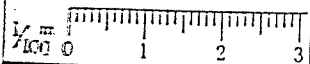
EVEN NO:
JUMPER TO +B

RESISTORS

NO.	VALUE	FUNCTION
1		
2		
3		
4		
5		

PROM

POS	PROM
F	CPU MC68701 V4S
	接続 PLC 出力ユニット: AX81



川鉄工業株式会社		図名 MCH. NAME	
殿		I/Oボード設定表	
課長 APPD	担当 DRAWN	尺取 SCALE	日付 DATE
中尾		free	20-May-'94
主任 CHKD	担当 DRAWN	機種 MCH. CD	箱番 BOX. NO
西田	坂本		
		図面番号 DRG. NO	第3角法 3RD ANGLE
		E.4:9.4:6.9.5.4	1/5
◎ 三菱三池製作所		流用図面 (保壳) 番号 OLD-DRG. (KEEPING)	
MITSUI MIKE			